





FIGURE 3 -- ENCODER POSITION GENERATOR

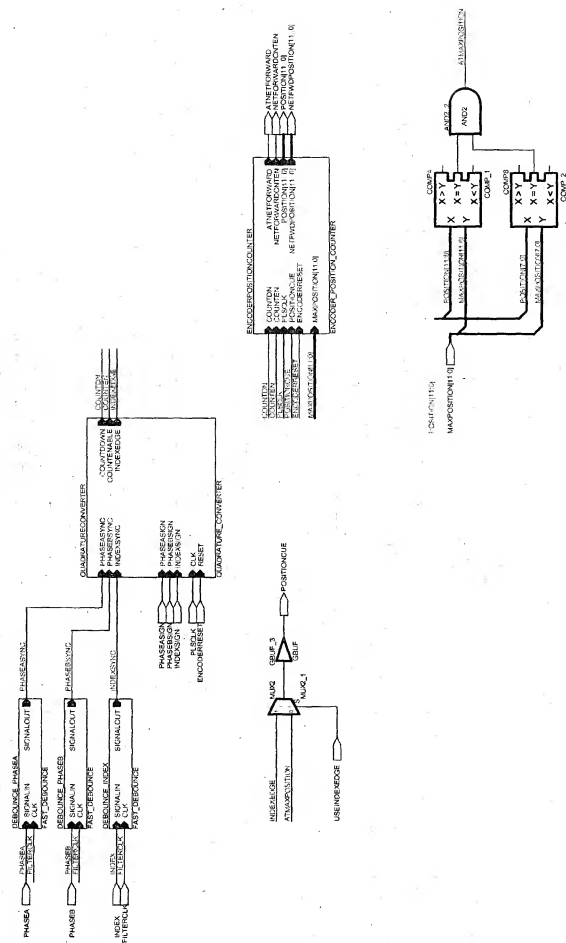


FIGURE 4 -- FAST SIGNAL DEBOUCE

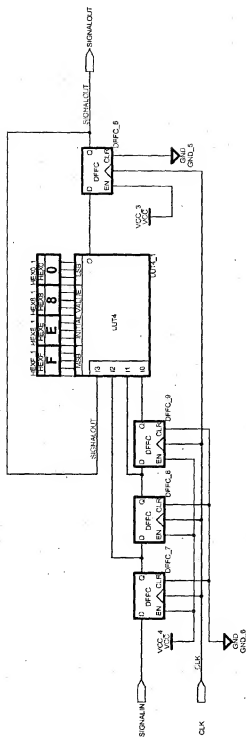




FIGURE 6 -- ENCODER POSITION COUNTER WITH NET FORWARD POSITION REGISTER

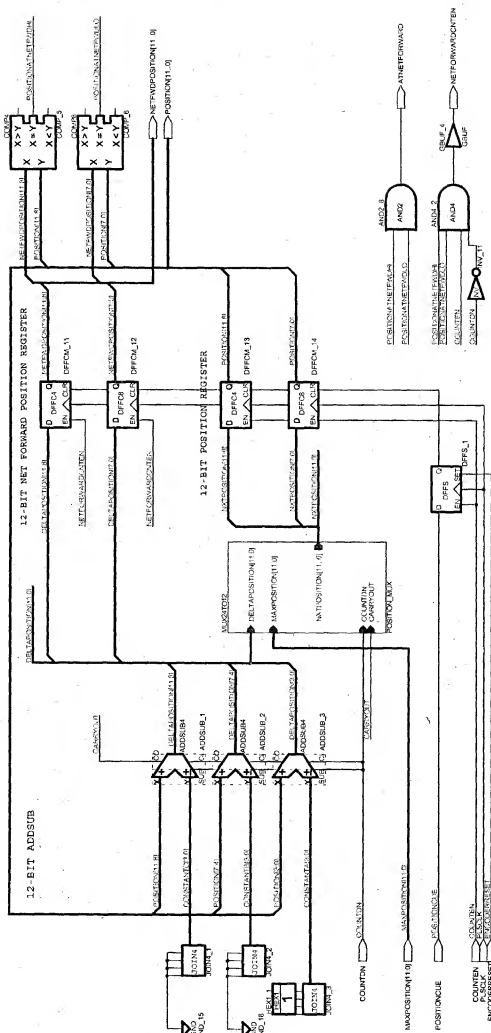


FIGURE 7 -- POSITION MULTIPLEXOR

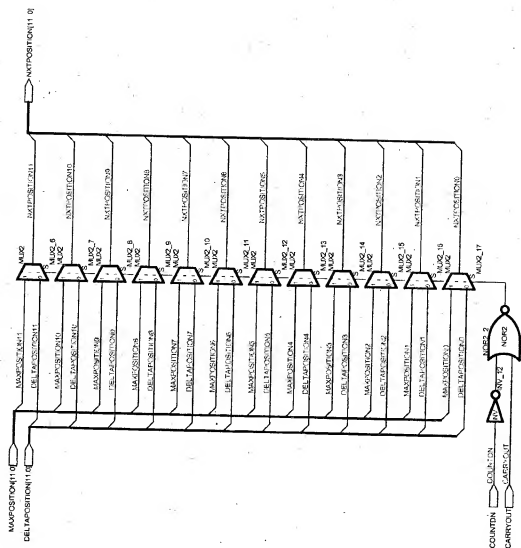












FIGURE 12 -- OUTPUT PULSE LOGIC

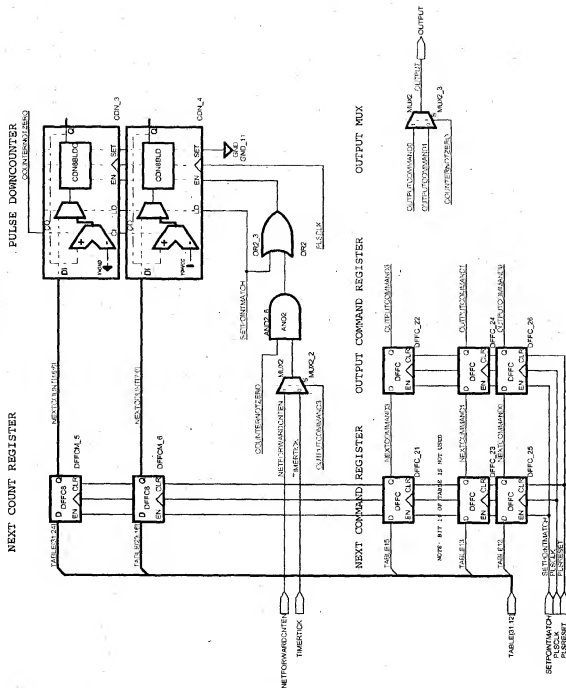




FIGURE 14 -- SETPOINT PULSE MODULE WITH OUTPUT PULSE COUNTER

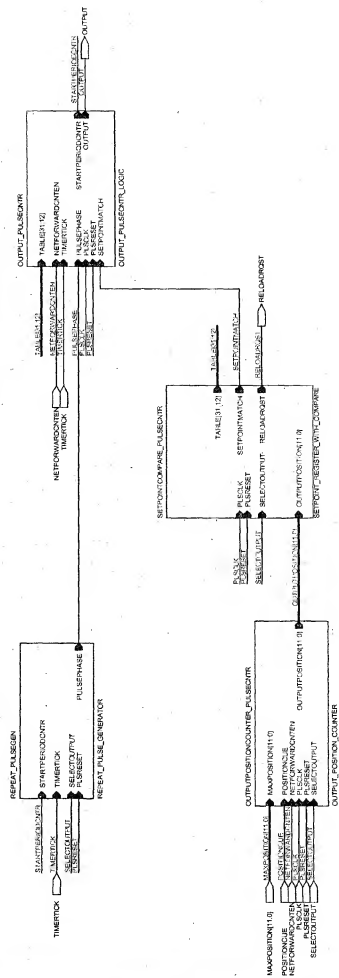


FIGURE 15 -- REPEAT PULSE GENERATOR

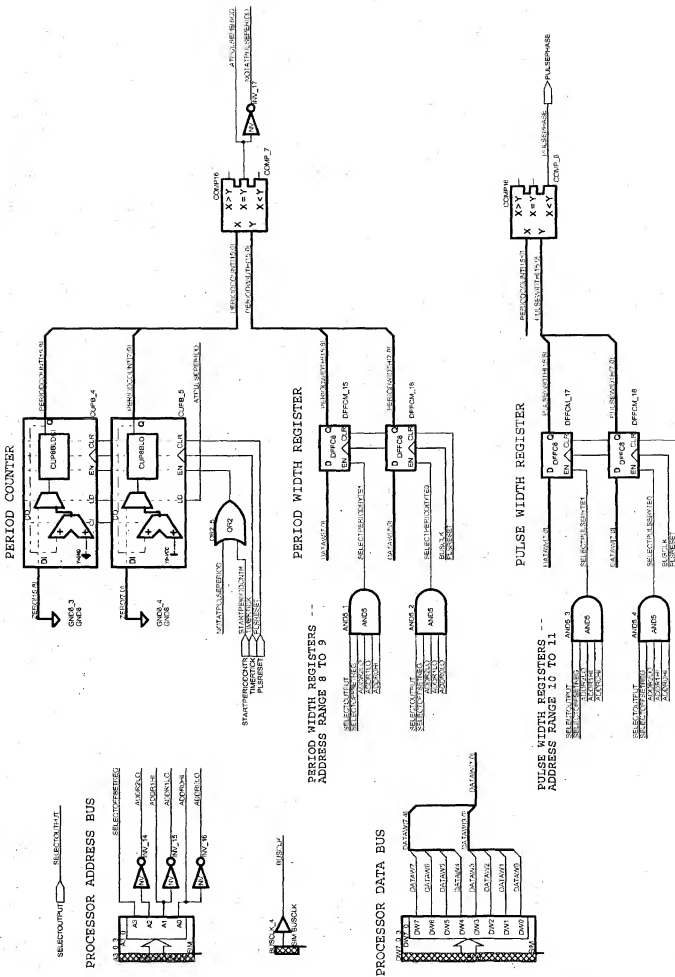








FIGURE 18 -- SPEED CHECK MODULE  
ROTATION COUNTER  
ROTATION TIME  
REGISTER

